

Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Page **1** of **5****Complete if Known**

Application Number	Not Yet Assigned
Filing Date	Herewith
First Named Inventor	MO et al.
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	018865-001740US

U.S. PATENT DOCUMENTS					
Examiner	Cite No. ¹	Document Number Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	4,070,690	01-24-1978	Wickstrom	
	AB	4,132,998	01-02-1979	Dingwall	
	AC	4,145,703	03-20-1979	Blanchard et al.	
	AD	4,326,332	04-27-1982	Kenney	
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	AF	4,344,081	08-10-1982	Pao et al.	
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	AH	4,398,339	08-16-1983	Blanchard et al.	
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	BH	5,321,289	06-14-1994	Baba, et al.	

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	BI	5,341,011	08-23-1994	Hshieh et al.	
	BJ	5,405,794	04-11-1995	Kim	
	BK	5,410,170	04-25-1995	Bulucea et al.	
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	BM	5,455,190	10-03-1995	Hsu	
	BN	5,460,985	10-24-1995	Tokura, et al.	
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Examiner
Signature*John W. Smith*Date
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[Signature]	CQ	5,801,408	09-01-1998	Takahashi	[Large X]
	CR	5,814,858	09-29-1998	Williams	
	CS	5,879,971	03-09-1999	Witek	
	CT	5,895,952	04-20-1999	Darwish et al.	
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	CX	5,998,837	12-07-1999	Williams	
	CY	6,015,737	01-18-2000	Tokura, et al.	
	CZ	6,049,108	04-11-2000	Williams et al.	
[Signature]	DA	6,204,533 B1	03-20-2001	Williams et al.	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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Examiner Signature	[Signature]	Date Considered	8/2/04
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	EG	JP	09102607	A	04-15-1997			<input type="checkbox"/>
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Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
KS	EM	BLANCHARD, R.A., "Optimization of discrete high power MOS transistors," <i>Stanford Electronics Laboratory, Integrated Circuits Laboratory</i> , April 1982, Technical Report No. IDEZ696-2.	/
	EN	CHANG, T.S. AND CRITCHLOW, D.L., "Verticle FET random-access memories with deep trench isolation," <i>IBM Technical Disclosure Bulletin</i> , January 1980, pp. 3683-3687, Vol. 22(8B).	/
	EO	GRANT, D.A. AND GOWER, J., "The development of power MOS devices," Chapter 1.2 In <i>Power Mosfets: Theory and applications</i> . John Wiley & Sons; New York, 1989, pp. 5-23.	/
	EP	GOODENOUGH, F., "Dense MOSFET enables portable power control," <i>Electronic Design</i> April 14, 1997, pp. 45-50.	/
	EQ	HOLMES, F.E., AND SALAMA, C.A.T., "V groove M.O.S. transistor technology," <i>Electronic Letters</i> , September 20, 1973, pp. 457-458, Vol. 9(19).	/
	ER	LIDOW, A. et al., "Power Mosfet technology," <i>IEEE Technical Digest-Int. Electron Devices Meet.</i> 1979, pp. 79-83.	/
	ES	LISIAK, K.P. and BERGER, J., "Optimization of nonplanar power MOS transistors," <i>IEEE Transactions on Electron Devices</i> , October 1978, pp. 1229-1234, Vol. Ed-25(10).	/
	ET	OU-YANG, P., "Double ion implanted V-MOS technology," <i>IEEE Journal of Solid-State Circuits</i> , February 1977, pp. 3-10, Vol SC-12(1).	/
	EU	SALAMA, C.A. AND OAKES, J.G., "Nonplanar power field-effect transistors," <i>IEEE Transactions on Electron Devices</i> , October 1978, pp. 1222-1228, Vol. Ed-25(10).	/
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MS	EW	SZE, S.M., "P-N- Junction diode," Chapter 2 In <i>Physics of Semiconductor Devices</i> . Second Edition, John Wiley & Sons; New York, 1981, pp. 63-108.	/

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